

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins
Term:	(voltage near3 ((ram or cache or (random\$2 near2 access)) same (flash or eprom or eeprom)))
Display:	10 Documents in <u>Display Format:</u> - Starting with Number 1
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Friday, May 13, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L10</u>	(voltage near3 ((ram or cache or (random\$2 near2 access)) same (flash or eprom or eeprom)))	9	<u>L10</u>
<u>L9</u>	(voltage near3 ((ram or cache or volatile) same (flash or eprom or eeprom)))	43	<u>L9</u>
<u>L8</u>	(voltage near3 ((ram or cache or volatile) and (flash or eprom or eeprom)))	317	<u>L8</u>
<u>L7</u>	(voltage near4 ((ram or cache or volatile) and (flash or eprom or eeprom)))	567	<u>L7</u>
<u>L6</u>	(voltage near3 level\$1 near3 ((ram or cache or volatile) and (flash or eprom or eeprom)))	7	<u>L6</u>
<u>L5</u>	L4 and (voltage near3 level\$1 near3 ((ram or cache or volatile) and (flash or eprom or eeprom)))	1	<u>L5</u>
<u>L4</u>	711/\$.ccls.	24503	<u>L4</u>
<u>L3</u>	L2 and voltage	1	<u>L3</u>
<u>L2</u>	20020120820.pn.	1	<u>L2</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			

L1 6272587.pn.

1 L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L25 and instruction\$1	1

Database:

US Pre-Grant Publication Full-Text Database
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 US OCR Full-Text Database
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 IBM Technical Disclosure Bulletins

Search:

L26

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Friday, May 13, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> <u>result</u> <u>set</u>
side by side			
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L26</u>	L25 and instruction\$1	1	<u>L26</u>
<u>L25</u>	6272587.pn.	1	<u>L25</u>
<u>L24</u>	11 and ((external\$2 near3 terminal) same (terminal near2 internal))	1	<u>L24</u>
<u>L23</u>	11 and ((external\$2 near3 terminal) same (second near2 internal))	0	<u>L23</u>
<u>L22</u>	11 and terminal	1	<u>L22</u>
<u>L21</u>	L13 and (((cop\$4 or mov\$3) with(volatile or ram or dram) with(non-volatile or non-volatile or flash or rom)) with between)	12	<u>L21</u>
<u>L20</u>	L13 and (((cop\$4 or mov\$3) near3 (volatile or ram or dram) near3(non-volatile or non-volatile or flash or rom)) with between)	2	<u>L20</u>
<u>L19</u>	L13 and (((cop\$4 or mov\$3) near3 (volatile or ram or dram) near3(non-volatile or non-volatile or flash or rom)) with from)	0	<u>L19</u>
<u>L18</u>	L13 and ((cop\$4 or mov\$3) near3 from near3 (volatile or ram or dram) near3 (non-volatile or non-volatile or flash or rom))	0	<u>L18</u>

<u>L17</u>	L13 and ((cop\$4 or mov\$3) near3 (volatile or ram or dram) near3(non-volatile or non-volatile or flash or rom))	74	<u>L17</u>
<u>L16</u>	L13 and ((cop\$4 or mov\$3) with(volatile or ram or dram) with (non-volatile or non-volatile or flash or rom) with bus)	8	<u>L16</u>
<u>L15</u>	L13 and ((cop\$4 or mov\$3) with(volatile or ram or dram) with (non-volatile or non-volatile or flash or rom))	203	<u>L15</u>
<u>L14</u>	L13 and ((cop\$4 or mov\$3) same (volatile or ram or dram) same (non-volatile or non-volatile or flash or rom))	324	<u>L14</u>
<u>L13</u>	711/103,104,105.ccls.	2543	<u>L13</u>
<u>L12</u>	110 and (cop\$4 or mov\$3)	1	<u>L12</u>
<u>L11</u>	(3906457 4818932 4847758 4961172 5001671 5226010 5386579 5404464 5606714 5652847 5655142 5664140 5680567 5682498 5813038 6026466 6209049)! [pn]	17	<u>L11</u>
<u>L10</u>	6480929.pn.	1	<u>L10</u>
<u>L9</u>	6408929.pn.	1	<u>L9</u>
<u>L8</u>	l3 and ((ram or dram) with rom with port with bus)	6	<u>L8</u>
<u>L7</u>	6480929.pn.	1	<u>L7</u>
<u>L6</u>	L3 and ((multipl\$5 or plural\$3) with bus\$2 with volatile with (non-volatile or nonvolatile))	5	<u>L6</u>
<u>L5</u>	L3 and (second with bus with (disk or disc or dasd)) and (first with bus with (ram or dram or sram))	7	<u>L5</u>
<u>L4</u>	L3 and (first with bus with (disk or disc or dasd)) and (second with bus with (ram or dram or sram))	1	<u>L4</u>
<u>L3</u>	711/112,104,105.ccls.	2696	<u>L3</u>
<u>L2</u>	L1 and arbiter	0	<u>L2</u>
<u>L1</u>	20020120820.pn.	1	<u>L1</u>

END OF SEARCH HISTORY

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Database:	US Pre-Grant Publication Full-Text Database	
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	JPO Abstracts Database	
	Derwent World Patents Index	
	IBM Technical Disclosure Bulletins	
Term:	memory near5 (vccq or vccf) near5 voltag\$2	
Display:	10	Documents in Display Format: TI
		Starting with Number 1
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image		

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side by side			
	DB=PGPB; PLUR=YES; OP=OR		
L6	memory near5 (vccq or vccf) near5 voltag\$2	4	L6
L5	memory near5 (vio or dq) near5 voltag\$2	10	L5
L4	memory near2 (vio or dq)near2 voltag\$2	0	L4
	DB=PGPB,USPT; PLUR=YES; OP=OR		
L3	l1 or l2	33	L3
	DB=PGPB; PLUR=YES; OP=OR		
L2	memory near2 ("I/O" or "input/output" or "i-o" or "input-output") near2 voltag\$2	15	L2
	DB=USPT; PLUR=YES; OP=OR		
L1	memory near2 ("I/O" or "input/output" or "i-o" or "input-output") near2 voltag\$2	18	L1

END OF SEARCH HISTORY